

Amendments to the Claims:

The following listing of claims will replace all prior versions, and listings, of claims in the application:

1-26. (Canceled)

27. (Currently Amended) A display device, comprising:

a display section having a plurality of scanning lines, a plurality of data lines and a plurality of dots corresponding to the plurality of scanning lines and the plurality of data lines, each of the plurality of dots including a display element and an active element;

a scanning line driver that drives the plurality of scanning lines;

a data line driver that drives the plurality of data lines;

a memory cell section having a plurality of memory cells, ~~the cell groups that~~ include the plurality of memory cells being arranged in a matrix having a plurality of rows and a plurality of columns; and

a selection switch section that controls transmission of an image signal to the plurality of memory cells, ~~cell groups, each of the plurality of memory cells being capable of storing an image signal supplied through the selection switch section, cell groups including~~ memory cells, the memory cell section being disposed between the display section and the selection switch,

the display section, the memory cell section, and the selection switch section being formed on one ~~substrate-substrate~~,

each of the plurality of memory cell groups storing first image signals supplied through the selection switch section, and

second image signals that are generated based on the first image signals being supplied to one data line of the plurality of data lines.

28. (Previously Presented) The display device according to claim 27, the memory cell section storing image signals for one screen.

29. (Currently Amended) The display device according to claim 27, further comprising:

a plurality of word lines among which a word line is selected when a memory cell of the plurality of memory cells receives the ~~image signal~~, first image signals,

the plurality of word lines extending along a row direction that intersects a column direction along which the plurality of data lines extend.

30. (Previously Presented) The display device according to claim 27, the number of the plurality of rows being equal to the number of the plurality of scanning lines.

31. (Previously Presented) The display device according to claim 27, further comprising a word line driver that drives the plurality of word lines.

32. (Currently Amended) The display device according to claim 27, further ~~comprising~~ comprising:

_____ a DAC section having a plurality of DACs,

each of the plurality of DACs receiving digital data based on the ~~image signal~~, first image signals,

each of the plurality of DACs converting the digital data into analog data.

33. (Previously Presented) The display device according to claim 27, each of the plurality of memory cells of the memory cell section being configured by any one of a dynamic memory and a static memory.

34. (Currently Amended) The display device according to claim 27, each of the memory cell of the memory cell section rewriting data based on the ~~image signal~~ first image signals without refreshing.

35. (Currently Amended) The display device according to claim 27, only when a change of display in one dot of the plurality of dots is carried out, a memory cell of the plurality of memory cells that corresponds to the one dot receiving the ~~image signal~~first image signals.

36. (Previously Presented) The display device according to claim 27, the memory cell section acting as a frame memory.

37. (Currently Amended) ~~A display device, comprising:~~The display device according to claim 27,

~~————— a display section having a plurality of scanning lines, a plurality of data lines and a plurality of dots corresponding to the plurality of scanning lines and the plurality of data lines, each of the plurality of dots including a display element and an active element;~~

~~————— a scanning line driver that drives the plurality of scanning lines;~~

~~————— a data line driver that drives the plurality of data lines;~~

~~a memory cell section having a plurality of memory cells, a length of the memory cell section in a row direction that intersects a column direction: along which the plurality of data lines extend being shorter than a length of the display section in the row direction; and~~

~~————— a selection switch section that controls transmission of an image signal to the plurality of memory cells, each of the plurality of memory cells being capable of storing an image signal supplied through the selection switch section, and the display section, the memory cell section, and the selection switch section being formed on one substrate.section.~~

38-44. (Canceled)

45. (Previously Presented) The display device according to claim 31, the word line driver receiving an address signal that is received by the scanning line driver.

46. (Original) The display device according to claim 31, further comprising a column decoder that selects a memory cell of the plurality of memory cells by cooperation with the word line.

47-48. (Canceled)

49. (Currently Amended) The display device according to ~~claim 47~~, claim 54, the number of the plurality of rows of the memory cell section being more than two.

50. (Canceled)

51. (Currently Amended) The display device according to ~~claim 47~~, claim 27, the number of the plurality of rows of the memory cell section being equal to the number of the plurality of scanning lines.

52. (Canceled)

53. (Currently Amended) The display device according to ~~claim 47~~, claim 27, a gray-scale level in the display section being obtained by at least one of an area gray-scale method, time-division gray-scale method and a method combining the area gray-scale method and the time-division gray-scale method.

54. (New) The display device according to claim 27, further comprising:
a DAC section that includes a plurality of DAC units, each of the plurality of DAC units receiving the first image signals.

55. (New) The display device according to claim 54, each of the plurality of DAC units being coupled to one data line of the plurality of data lines.

56. (New) The display device according to claim 27, further comprising:
a sense amplifier section.

57. (New) The display device according to claim 56, further comprising:
a DAC section that includes a plurality of DAC units, the DAC section being disposed between the sense amplifier section and the display section.

58. (New) The display device according to claim 27, the memory cell section further including a memory row decoder for operating the plurality of memory cells, the memory row decoder and the scanning line driver receiving a common controlling signal.